Γεώργιος Σταμούλης

Εργασία:
Πανεπιστήμιο Θεσσαλίας
Τμήμα Ηλεκτρολόγων Μηχανικών και Μηχανικών Η/Υ

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Επιστημονικά ενδιαφέροντα

Σχεδιασμός κυκλωμάτων χαμηλής ισχύος, επαλήθευση και αξιοπιστία ολοκληρωμένων κυκλωμάτων, υπολογισμός μέσης και μέγιστης ισχύος, σχεδιασμός συστημάτων λογισμικού CAD. Θεωρία βελτιστοποίησης και εφαρμογές. Ασύρματα δίκτυα αισθητήρων και εφαρμογές. Ανανεώσιμες πηγές ενέργειας (γεωθερμία).

Εκπαίδευση

1991—1994
University of Illinois — Urbana, Illinois
Διδακτορική διατριβή στο Τμήμα Ηλεκτρολόγων και Μηχανικών Ηλεκτρονικών Υπολογιστών με τίτλο: Πιθανοθεωρητική εξομοίωση για αξιοπιστία και υπολογισμό μέσης ισχύος.
Επιβλέπων: Ibrahim Hajj

1989—1991
University of Illinois — Urbana, Illinois
Master of Science στο Τμήμα Ηλεκτρολόγων και Μηχανικών Ηλεκτρονικών Υπολογιστών με τίτλο: Νέες τεχνικές πιθανοθεωρητικής εξομοίωσης VLSI CMOS κυκλωμάτων.
Επιβλέπων: Ibrahim Hajj

1984—1989
Εθνικό Μετσόβιο Πολυτεχνείο — Αθήνα
Δίπλωμα από το Τμήμα Ηλεκτρολόγων και Μηχανικών Ηλεκτρονικών Υπολογιστών.
Εκπόνηση διπλωματικής εργασίας με τίτλο: Σχεδίαση και ανάλυση δομικών στοιχείων για τηλεπικοινωνιακά συστήματα υψηλού ρυθμού μετάδοσης.
Επιβλέπων: Ιωάννης Αβαριτσιώτης.

Εμπειρία

Ερευνητική

Ιανουάριος 2009—
Πανεπιστήμιο Θεσσαλίας — Βόλος
Καθηγητής

Ιανουάριος 2003—Ιανουάριος 2009
Πανεπιστήμιο Θεσσαλίας — Βόλος
Αναπληρωτής Καθηγητής

Ιούνιος 2001—Ιανουάριος 2003
Πολυτεχνείο Κρήτης — Κουνουπιδιανά
Επίκουρος Καθηγητής

Ιανουάριος 2000—Ιούνιος 2001
Intel Corp. — Χάιφα, Ισραήλ
Επικεφαλής της ομάδας χαμηλής ισχύος του Mobile Processor Group για το Centrino.

Απρίλιος 1999—Ιανουάριος 2000
Intel Corp. — Santa Clara, California
Διευθυντής των Strategic CAD Laboratories – τμήμα Santa Clara, και επικεφαλής της ομάδας χαμηλής ισχύος και αξιοπιστίας των Strategic CAD Laboratories.

Ιανουάριος 1999—Φεβρουάριος 1999.
Υπουργείο Εθνικής Άμυνας. — Αθήνα
Εξωτερικός συνεργάτης της Επιτροπής Έρευνας, Τεχνολογίας και Πληροφορικής.

Αύγουστος 1997—Απρίλιος 1999
Intel Corp. Santa Clara, California
Επικεφαλής της ομάδας CAD χαμηλής ισχύος για τους επεξεργαστές νέας γενιάς (παραγωγή μετά το 2002).
Ιούνιος 1995—Αύγουστος 1997
Intel Corp. Santa Clara, California
Sr. CAD Engineer. Σχεδιασμός κυκλωμάτων και συστημάτων CAD χαμηλής ισχύος. Τέσσερεις αιτήσεις για δίπλωμα ευρεσιτεχνίας (δύο που αφορούσαν κυκλώματα, τα οποία στη συνέχεια έγιναν διπλώματα ευρεσιτεχνίας, και δύο που αφορούσαν
βελτιστοποιήσεις μεταγλωττιστών και παρέμειναν απόρρητα της Intel Corp.)
Αύγουστος 1994-- Ιούνιος 1995
University of Iowa Iowa City, Iowa
Εξομοίωση κυκλωμάτων για αξιοπιστία και χαμηλή ισχύ.
1989—1994
Coordinated Science Laboratory Urbana, Illinois
Πιθανοθεωρητική εξομοίωση για αξιοπιστία και χαμηλή ισχύ.
1987—1989
Εθνικό Μετσόβιο Πολυτεχνείο Αθήνα
Συμμετοχή στο πρόγραμμα RACE, φάσεις 4.0 μέχρι 4.4.

Διδακτική

Αύγουστος 1994—Ιούνιος 1995
University of Iowa Iowa City, Iowa
Επισκέπτης Επίκουρος Καθηγητής
Εισαγωγή στον σχεδιασμό κυκλωμάτων VLSI (Φθινόπωρο 1994, μεταπτυχιακό). Προχωρημένος σχεδιασμός κυκλωμάτων VLSI (Άνοιξη 1995, μεταπτυχιακό).
Ιούνιος 2001—Ιανουάριος 2003
Πολυτεχνείο Κρήτης Χανιά
Σχεδιασμός Συστημάτων VLSI και ASIC (Φθινόπωρο 2001 και 2002).
Σχεδιασμός Ψηφιακών Κυκλωμάτων με Εργαλεία CAD (Φθινόπωρο 2001 ΠΜΣ) Λογική Σχεδίαση Ψηφιακών Συστημάτων (Άνοιξη 2002).
Σεπτέμβριος 2001 –
Πανεπιστήμιο Θεσσαλίας Βόλος
Ψηφιακή Σχεδίαση Ι (Φθινόπωρο 2001-2007 & 2013—2018) Ψηφιακή Σχεδίαση ΙΙ με CAD (Άνοιξη 2002 και 2003) Ανάλυση Κυκλωμάτων (Φθινόπωρο 2009 και 2010)
Ηλεκτρονική Ι (Φθινόπωρο 2010 και 2012)
Σχεδίαση Συστημάτων VLSI (Άνοιξη 2003-2012, 2015-2019)
Έλεγχος και Επαλήθευση Ολοκληρωμένων Κυκλωμάτων (Φθινόπωρο 2003-2006) Σχεδίαση Μικροεπεξεργαστών (Φθινόπωρο 2004 ΠΜΣ)
Σχεδίαση Συστημάτων VLSI (Άνοιξη 2006-2019 ΠΜΣ)
Σχεδίαση Εργαλείων CAD (Φθινόπωρο 2006-2018 ΠΜΣ)
Σχεδίαση Χαμηλής Ισχύος (Άνοιξη 2011-2015 ΠΜΣ)
Αρχιτεκτονική Υπολογιστών (Φθινόπωρο 2006 ΠΜΣ)
Εισαγωγή στο Ηλεκτρολογικό Σχέδιο και στα Ηλεκτροτεχνικά Υλικά (Άνοιξη 2013—2015)
Δίκτυα Αισθητήρων (Άνοιξη 2013—2015)
Εισαγωγή στη Φυσική Στερεάς Κατάστασης (Φθινόπωρο 2013—2018 ΠΜΣ)
Αλγόριθμοι CAD για Φυσική Σχεδίαση (Άνοιξη 2016-2018 ΠΜΣ)

Διοικητική

Ιούλιος 2023 –
Πανεπιστήμιο Θεσσαλίας Βόλος
Μέλος του Συμβουλίου Διοίκησης.
Ιούλιος 2018 –2023
Πανεπιστήμιο Θεσσαλίας Βόλος
Κοσμήτορας Πολυτεχνικής Σχολής.

Ιούλιος 2013 – Ιούλιος 2018
Πανεπιστήμιο Θεσσαλίας Λαμία
Πρόεδρος Τμήματος Πληροφορικής.

Σεπτέμβριος 2007 – Αύγουστος 2011
Πανεπιστήμιο Θεσσαλίας Βόλος
Πρόεδρος Τμήματος Μηχανικών Η/Υ, Τηλεπικοινωνιών και Δικτύων.

Σεπτέμβριος 2003 – Αύγουστος 2007
Πανεπιστήμιο Θεσσαλίας Βόλος
Αναπληρωτής Πρόεδρος Τμήματος Μηχανικών Η/Υ, Τηλεπικοινωνιών και Δικτύων.

Σεπτέμβριος 2005 – Αύγουστος 2007
Πανεπιστήμιο Θεσσαλίας Βόλος
Διευθυντής Προγράμματος Μεταπτυχιακών Σπουδών

Σεπτέμβριος 2004 – Αύγουστος 2005 Πανεπιστήμιο Θεσσαλίας Βόλος
Εκπρόσωπος των Αναπληρωτών Καθηγητών στη Σύγκλητο

Σεπτέμβριος 2007 – Αύγουστος 2011 Πανεπιστήμιο Θεσσαλίας Βόλος
Μέλος της Επιτροπής Ερευνών

Συμμετοχή σε Επιτροπές Διαγωνισμών, Μετεγγραφών, Αξιολόγησης Υποψηφίων για το ΠΜΣ, Προγράμματος Σπουδών, Κατατακτηρίων Εξετάσεων, Αξιολόγησης Κατατασσομένων από το ΔΙΚΑΤΣΑ/ΔΟΑΤΑΠ, Επιλογής Διοικητικού Προσωπικού, Επιλογής Υποψηφίων για θέσεις του ΠΔ 407/80, ΣΕΜΣ.

Συμμετοχή σε πάνω από δεκαπέντε (15) εισηγητικές επιτροπές εκλεκτορικών σωμάτων για μέλη ΔΕΠ και σε τρεις (3) εισηγητικές επιτροπές εκλεκτορικών σωμάτων για μέλη ΕΤΕΠ.

Καινοτόμες επιχειρήσεις

Νοέμβριος 2007—Δεκέμβριος 2015 Sensap A.E. Αθήνα
Ιδρυτής, Chief Technical Officer

Νοέμβριος 2008— Δεκέμβριος 2014 Nanotropic A.E. Αθήνα
Ιδρυτής, Chief Scientist

Επιστημονικός Σύμβουλος

Αύγουστος 1993
Advanced Micro Devices San Jose, California
Σύμβουλος για θέματα αξιοπιστίας και χαμηλής ισχύος.

Ιούνιος 2003—Μάρτιος 2004
Οργανισμός Προώθησης Ελληνικού Πολιτισμού Αθήνα
Σύμβουλος για ψηφιακές πλατφόρμες μουσείων

Σεπτέμβριος 2009—Μάρτιος 2010
Παρατηρητήριο για την Κοινωνία της Πληροφορίας Αθήνα
Σύμβουλος για υποδομές GIS

Ιούνιος 2008—Δεκέμβριος 2014
Helic A.E. Αθήνα
Σύμβουλος σε θέματα προσομοίωσης ψηφιακών κυκλωμάτων.

Reviews

IEEE Transactions on Computers
IEEE Transactions on VLSI Systems
IEEE Transactions on Computer Aided Design Sensors & Actuators: A. Physical.

Μέλος τεχνικής επιτροπής του Custom Integrated Circuit Conference (1996-1997) Μέλος τεχνικής επιτροπής και session chair στο International Symposium on Low Power Design (1999-2007)

Μέλος τεχνικής επιτροπής του International On-Line Testing Workshop (2000-2006) Μέλος τεχνικής επιτροπής του Design and Test in Europe 2001, 2004-2006

Μέλος τεχνικής επιτροπής του ISQED 2001

Μέλος της οργανωτικής επιτροπής του PCI 2005

Δημοσιεύσεις

Thesis

Georgios I. Stamoulis. “Design and Analysis of High Transmission Rate Telecommunication Circuit Elements.” Diploma Thesis. Department of Electrical and Computer Engineering. National Technical University of Athens, June 1989.

Georgios I. Stamoulis. “New techniques for probabilistic simulation of VLSI CMOS circuits.” Master’s Thesis. Department of Electrical and Computer Engineering.
University of Illinois at Urbana-Champaign, September 1991.

Georgios I. Stamoulis. “Probabilistic simulation for reliability and average power estimation.” Ph.D. Thesis. Department of Electrical and Computer Engineering.
University of Illinois at Urbana-Champaign, April 1994.

Συνέδρια με κριτές

P. -C. Li, G. I. Stamoulis, and I. N. Hajj, “A probabilistic timing approach to hot-carrier effect estimation”. Proceedings of the IEEE/ACM Conference on Computer Aided Design, pp. 210-213, November 1992.

G. I. Stamoulis, and I. N. Hajj, “Improved techniques for probabilistic simulation including signal correlation effects”. Proceedings of the 30th ACM/IEEE Design Automation Conference, pp. 379-383, June 1993.

P. -C. Li, G. I. Stamoulis, and I. N. Hajj, “iPROBE-d: A hot-carrier and oxide reliability simulator”. Proceedings of the International Reliability Physics Symposium 1994, pp. 274-279.

G. I. Stamoulis, and I. N. Hajj, “Slope considerations in probabilistic simulation”. Proceedings of the 1994 Custom Integrated Circuits Conference, pp. 186-190, May 1994.

G. I. Stamoulis, “A Monte-Carlo approach for the accurate and efficient estimation of average transition probabilities in sequential logic circuits”. Proceedings of the 1996 Custom Integrated Circuits Conference, pp. 221-224.

G. I. Stamoulis et al. “Transistor-level optimization”. Proceedings of the Design Technology and Test Conference 1997. Invited paper.

Y. Ye, K. Roy, and G. I. Stamoulis, “Quasi-static energy recovery logic and supply clock generation circuits”. Proceedings of the 1997 International Symposium on Low Power Design, pp. 96-99.

N. Bellas, I. Hajj, C. Polychronopoulos, and G. Stamoulis, “Architectural and compiler support for energy reduction in the memory hierarchy of high- performance microprocessors”. Proceedings of the 1998 International Symposium on Low Power Design, pp. 70-75. June 1998.

N. Bellas, I. Hajj, C. Polychronopoulos, and G. Stamoulis, “A new scheme for I- Cache energy reduction in High Performance Processors”. Power-Driven Microarchitectures Workshop, International Symposium On Computer Architecture (ISCA), June 1998, Barcelona, Spain

U. K. Narayanan, R. K. Roy, and G. I. Stamoulis, “Characterizing individual gate power sensitivity in low power design”. Proceedings of VLSI-India 1999, pp. 625- 628.

N. Bellas, I. Hajj, C. Polychronopoulos, and G. Stamoulis, “Energy and Performance Improvements in Microprocessor Design using a Loop Cache”. Proceedings of ICCD ’99, pp. 378-383.

P. D. Dimitropoulos, C. Kachris, and G. I. Stamoulis, “A New SOI capacitive sensor for absolute and differential pressure measurements,” Digest 18th European Conf. Solid State Sensors (EUROSENSORS 2004), September 2004, Rome – Italy, pp. 60-61.

P. D. Dimitropoulos, S. P. Nikolaidis, D. P. Karampatzakis, and G. I. Stamoulis, “A low-power CMOS VLSI circuit for signal conditioning in integrated capacitive sensors,” IEEE Proc. 3rd International Conf. Sensors (Sensors 2004), October 2004,
Vienna, Austria, pp. 202–205.

P. D. Dimitropoulos, E. Hristoforou, and G. I. Stamoulis, “A hybrid Jiles-Atherton / Stoner-Wolfarth magnetic hysteresis model for inductive sensors and actuators,” IEEE Proc. 3rd International Conf. Sensors (Sensors 2004), October 2004, Vienna,
Austria, pp. 1566–1569.

N. E. Evmorfopoulos, D. P. Karampatzakis, and G. I. Stamoulis, “Voltage-Drop-
Constrained Optimization of Power Distribution Network Based on Reliable Maximum Current Estimates”, ICCAD ’04, pp. 479- 484.

D. P. Karampatzakis, N. E. Evmorfopoulos and G. I. Stamoulis, “A Statistically-
Based Engine for P/G Network Optimization”, IEEE PRIME ’05, pp. 51- 54.

G. Dimitriou, P. K. Kikiras, G. I. Stamoulis, and I. N. Avaritsiotis. “A Tool for Calculating Energy Consumption in Wireless Sensor Networks”, Proceedings of the 10th Panhellenic Conference on Informatics 2005, pp. 611-621.

G. Stamoulis, M. Koziri, I. Katsavounidis, and N. Bellas, “A Low-Power VLSI Architecture for Intra Prediction in H.264”, Proceedings of the 10th Panhellenic
Conference on Informatics 2005, pp. 633-640.

D. Karampatzakis, N. Evmorfopoulos, M. Tsiampas, and G. Stamoulis, “An RTL-to- grid design flow for power grid verification based on a statistical estimation
engine”, IEEE PRIME, Lecce, Italy, 2006.IEEE PRIME ’06, pp. 37-40.

M. G. Koziri, G. I. Stamoulis, and I. X. Katsavounidis, “A Low-Power VLSI Architecture for Intra Prediction in H.264”, IEEE PRIME ’06, pp. 109-112.

D. Bountas, and G. I. Stamoulis, “CARROT: A Tool for Fast and Accurate Soft Error
Rate Estimation”, 6th International Workshop, SAMOS 06, pp.331-338.

N. Evmorfopoulos, D. P. Karampatzakis, and G. I. Stamoulis, “Precise Identification of the Worst-Case Voltage Drop Conditions in Power Grid Verification”, ICCAD
’06, pp. 112-118.

M. G. Koziri, G. I. Stamoulis, and I. X. Katsavounidis, “Power reduction in H.264 encoder through algorithmic and circuits transformation” ISLPED 2006, pp. 107-
112.

M. G. Koziri, A. N. Dadaliaris, G. I. Stamoulis, and I. X. Katsavounidis, “A Novel
Low-Power Motion Estimation Design for H.264”, ASAP 2007.

P. Kikiras, D. Drakoulis, D. Dres, and G. I. Stamoulis, “Wireless Sensor Networks: Business Models and Market Issues”. 6th Conf. Telecomm Technoeconomics,
2007, Helsinki, Finland.

K. Katsalis, A. Xenakis, K. Kikiras, and G. Stamoulis, “Topology Optimization in Wireless Sensor Networks for Precision Agriculture Applications” “International Conference on Sensor Technologies and Applications SENSORCOMM 2007,
Valencia, Spain, pp. 526-530.

K. Katsalis, A. Xenakis, P. Kikiras, and G. Stamoulis, “Μεθοδολογίες βελτιστοποίησης της τοπολογίας ασυρμάτων δικτύων αισθητήρων σε εφαρμογές γεωργίας ακριβείας” (Wireless sensor network topology optimization for precision agriculture applications). 5o Συνέδριο Ε.Γ.Μ.Ε. (5th National Agricultural Engineering Congress), 2007, Larissa, Greece.

K. Katsalis, A. Xenakis, K. Kikiras, and G. Stamoulis, “Topology Optimization in Wireless Sensor Networks for Precision Agriculture Applications”. 6th European
Conference on Precision Agriculture, 2007, Skiathos, Greece.

D. P. Karampatzakis, M. K. Tsiampas, N. E. Evmorfopoulos, and G. I. Stamoulis, “A Design Flow for the Precise Identification of the Worst-Case Voltage Drop in Power Grid Analyses”. Panhellenic Conference on Informatics, 2008 (PCI '08), pp. 135 – 139.

P. Kikiras, L. Perlepes, I. Kalavros, A. Tzimas, and G. Stamoulis, “Sensor Networks Applications in Livestock Production: the Case of Fleming Institute’s Animal House”. 2nd Panellehnic Conference of Livestock Breeding, 2008, Larissa, Greece.

P. Kikiras, L. Perlepes, and G. Stamoulis, “Wireless Sensor Networks in Gaming”. 1st Workshop on Wireless Sensor Networks, 2008, Athens, Greece.

D. Bountas, G. Stamoulis, and N. Evmorfopoulos, “A macromodel technique for VLSI dynamic simulation by mapping pre-characterized transitions”. IEEE International Conference on Computer Design, 2008 (ICCD 2008), pp. 450 – 456.

N. Papadakis, K. Ntalianis, A. Doulamis, and G. Stamoulis, “An Automatic Multi- Agent Web Image and Associated Keywords Retrieval System”. 16th International Conference on Systems, Signals and Image Processing, 2009 (IWSSIP 2009), pp. 1- 4.

M. Owaida, M. Koziri, I. Katsavounidis, and G. Stamoulis, “A high performance and low power hardware architecture for the transform and quantization stages in H.264”. IEEE International Conference on Multimedia and Expo, 2009 (ICME 2009), pp. 1102 – 1105.

A. Xenakis, L. Perlepes, P Kikiras, and G. Stamoulis. “Application of the LGR Algorithm for topology control in a wireless sensor network for precision agriculture applications”. Panhellenic Conference of the Society of Agricultural Engineers, 2009, Thessaloniki Greece, pp. 641-648.

C. Chatziplaton, G. I. Stamoulis, and P.Kikiras, “Vehicle speed and position determination using WSN”. 2nd Workshop on Wireless Sensor Networks, 2009, Athens, Greece.

G. Giannakas, F. Plessas, G. Nassopoulos, and G. Stamoulis, “A 2.45GHz power harvesting circuit in 90nm CMOS”. 17th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2010, pp. 835 – 838.

M. K. Tsiampas, D. Bountas, P. Merakos, N. E. Evmorfopoulos, S. Bantas, and G. I. Stamoulis, “A power grid analysis and verification tool based on a Statistical Prediction Engine”. 17th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2010, pp. 839 – 842.

A. Zaharis, A. I. Martini, P. Kikiras, and G. Stamoulis, “User Authentication Method and Implementation Using a Three-Axis Accelerometer”. MOBILIGHT 2010, pp. 192-202.

A. Raman, D. Bountas, and G. Stamoulis, “A High-Capacity Power Integrity Flow Supporting Inductive Rail Effects With Transistor-Level Accuracy”. Magma Users Summit on Integrated Circuits 2010 (Best paper award).

A. Zaharis, A. I. Martini, L. Perlepes, G. Stamoulis, and P. Kikiras, “Live forensics framework for wireless sensor nodes using sandboxing”. Proceedings of the 6th ACM workshop on QoS and security for wireless and mobile networks 2010 (Q2SWinet '10), pp. 70-77.

N. Evmorfopoulos, M. –A. Rammou, G. Stamoulis, and J. Moondanos, “Characterization of the worst-case current waveform excitations in general RLC- model power grid analysis”. 2010 IEEE/ACM International Conference on

Computer-Aided Design (ICCAD), pp. 824 – 830.

V. N. Petoussis, P. D. Dimitropoulos, G. Stamoulis, and E. Houstis, "Novel Dynamic Technique Reducing the Offset Voltage in a Hall Effect Sensor". 18th Sensor + Test, 2011, pp. 780 – 785.

V. N. Petoussis, P. D. Dimitropoulos, G. Stamoulis, and E. Houstis, "Introducing a New Hall Effect Sensor- Novel Dynamic Offset Reduction Method", 1st International Conference on Pervasive and Embedded Computing and Communication Systems (PECCS) Algarve Portugal, 2011.

A. Zaharis, L. Perlepes, G. Stamoulis, and P. Kikiras, "End – to – End Secure Data Delivery in Wireless Sensor Networks". SENSORCOMM 2011, pp. 356-364.

F. Liakou, P. Dimitropoulos, and G. Stamoulis, "2D Simulator for Eddy Current Field Filters". Eurosensors XXV, 2011.

M. –A. Nef, S. Karagiorgou, G. I. Stamoulis, and P. K. Kikiras, "Supporting Service Differentiation in Wireless Sensor Networks". 15th Panhellenic Conference on Informatics (PCI), 2011, pp. 127 – 133.

M. –A. Nef, I. Filippopoulos, E. Voumvourakis, A. Aggelis, L. Perlepes, G. Stamoulis, and P. Kikiras, "i-Protect: An Open Source Emergency Management Framework". 15th Panhellenic Conference on Informatics (PCI), 2011, pp. 386 – 391.

F. Toufexis, A. Papanikolaou, D. Soudris, G. Stamoulis, and S. Bantas, "Power, Performance and Area Prediction of 3D ICs during Early Stage Design Exploration in 45nm". 18th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2011, pp. 715 - 718.

N. Larisis, L. Perlepes, P. Kikiras, and G. Stamoulis, "U-Park: Parking Management System Based on Wireless Sensor Network Technology". SensorComm 2012, pp. 170-177.

M. –A. Nef, L. Perlepes, S. Karagiorgou, G. I. Stamoulis, and P. K. Kikiras, "Enabling QoS in the Internet of Things". Fifth International Conference on Communication Theory, Reliability, and Quality of Service (CTRQ) 2012, pp. 33-38.

K. Daloukas, M.-A. Rammou, G. Drasidis, M. Tsiampas, N. Evmorfopoulos, P. Tsompanopoulou, and G. Stamoulis, "Parallel Preconditioners Based on Fast Poisson Solvers for Efficient Large-Scale Power Grid Analysis". ACM/IEEE 2012 Design Automation Conference, Poster. San Francisco, June 2012.

K. Daloukas, N. Evmorfopoulos, P. Tsompanopoulou, and G. Stamoulis, "Fast Transform-Based Solvers as Parallel Preconditioners for Large-Scale Power Grid Analysis on Massively Parallel Architectures". Poster at the ACM Student Research Competition, held in conjunction with the ACM/IEEE 2012 Design Automation Conference. San Francisco, June 2012.

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Επίβλεψη διδακτορικών	Δεκαεπτά (17) ολοκληρώθηκαν.
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Αναφορές	Πάνω από 1000

Ερευνητικά προγράμματα

- Intel Corp. δωρεά $45000 και μηχανημάτων για το εργαστήριο
- Θέων Αισθητήρες ΑΕΒΕ. €229000.
  Ανάπτυξη αισθητήρων πίεσης με τεχνολογία MEMS
- ΠΕΝΕΔ 2003. €106000.
  Βελτιστοποίηση κυκλωμάτων VLSI ως προς το κύκλωμα τροφοδοσίας.
- NETNET €15000.
  Σεμινάρια επιμόρφωσης Ηλεκτρολόγων Μηχανικών
- ΥΠΠΟ €6000.
  Ανάπτυξη συστήματος περιβαλλοντικών μετρήσεων.
- iMuse €500000.
  Ανάπτυξη συστήματος πλοήγησης σε μουσεία βασισμένο σε τεχνολογία RFID.
- Nanotropic Spin-off €600000.
  Αξιόπιστος υπολογισμός πτώσης τάσης σε κυκλώματα τροφοδοσίας ολοκληρωμένων κυκλωμάτων.
- ECOSYSTEM €40000.
  Ανάπτυξη συστήματος Smart Home βασισμένο σε FPGAs.
- NanoTrim €1200000.
  Βελτιστοποίηση μεγέθους τρανζίστορ ολοκληρωμένων κυκλωμάτων στο φυσικό επίπεδο.
- Horizon 2020: Solder €450,000
- Horizon 2020: Enforce €50,000
- ΓΓΕΤ: Ερευνώ Καινοτομώ €270,000