

Γεώργιος Σταμούλης

Εργασία:

Πανεπιστήμιο Θεσσαλίας
Τμήμα Ηλεκτρολόγων Μηχανικών και Μηχανικών Η/Υ

Γκλαβάνη 37
Βόλος 38221

Τηλ: 24210-74971
e-mail: georges@uth.gr

Επιστημονικά ενδιαφέροντα

Σχεδιασμός κυκλωμάτων χαμηλής ισχύος, επαλήθευση και αξιοπιστία ολοκληρωμένων κυκλωμάτων, υπολογισμός μέσης και μέγιστης ισχύος, σχεδιασμός συστημάτων λογισμικού CAD. Θεωρία βελτιστοποίησης και εφαρμογές. Ασύρματα δίκτυα αισθητήρων και εφαρμογές. Ανανεώσιμες πηγές ενέργειας (γεωθερμία).

Εκπαίδευση

1991—1994
University of Illinois Urbana, Illinois

Διδακτορική διατριβή στο Τμήμα Ηλεκτρολόγων και Μηχανικών Ηλεκτρονικών Υπολογιστών με τίτλο: Πιθανοθεωρητική εξομοίωση για αξιοπιστία και υπολογισμό μέσης ισχύος.

Επιβλέπων: Ibrahim Hajj
1989—1991

University of Illinois Urbana, Illinois

Master of Science στο Τμήμα Ηλεκτρολόγων και Μηχανικών Ηλεκτρονικών Υπολογιστών με τίτλο: Νέες τεχνικές πιθανοθεωρητικής εξομοίωσης VLSI CMOS κυκλωμάτων.

Επιβλέπων: Ibrahim Hajj
1984—1989

Εθνικό Μετσόβιο Πολυτεχνείο Αθήνα

Δίπλωμα από το Τμήμα Ηλεκτρολόγων και Μηχανικών Ηλεκτρονικών Υπολογιστών.

Εκπόνηση διπλωματικής εργασίας με τίτλο: Σχεδίαση και ανάλυση δομικών στοιχείων για τηλεπικοινωνιακά συστήματα υψηλού ρυθμού μετάδοσης.

Επιβλέπων: Ιωάννης Αβαριτσιώτης.

Εμπειρία

Ερευνητική

Ιανουάριος 2009—

Πανεπιστήμιο Θεσσαλίας Βόλος

Καθηγητής

Ιανουάριος 2003—Ιανουάριος 2009

Πανεπιστήμιο Θεσσαλίας Βόλος

Αναπληρωτής Καθηγητής

Ιούνιος 2001—Ιανουάριος 2003

Πολυτεχνείο Κρήτης Κουνουπιδιανά

Επίκουρος Καθηγητής

Ιανουάριος 2000—Ιούνιος 2001

Intel Corp. Χάιφα, Ισραήλ

Επικεφαλής της ομάδας χαμηλής ισχύος του Mobile Processor Group για το Centrino.

Απρίλιος 1999—Ιανουάριος 2000

Intel Corp. Santa Clara, California

Διευθυντής των Strategic CAD Laboratories – τμήμα Santa Clara, και επικεφαλής της ομάδας χαμηλής ισχύος και αξιοπιστίας των Strategic CAD Laboratories.

Ιανουάριος 1999—Φεβρουάριος 1999.

Υπουργείο Εθνικής Άμυνας. Αθήνα

Εξωτερικός συνεργάτης της Επιτροπής Έρευνας, Τεχνολογίας και Πληροφορικής.

Αύγουστος 1997—Απρίλιος 1999

Intel Corp. Santa Clara, California

Επικεφαλής της ομάδας CAD χαμηλής ισχύος για τους επεξεργαστές νέας γενιάς (παραγωγή μετά το 2002).

	Ιούνιος 1995—Αύγουστος 1997	
	<u>Intel Corp.</u>	Santa Clara, California
	Sr. CAD Engineer. Σχεδιασμός κυκλωμάτων και συστημάτων CAD χαμηλής ισχύος. Τέσσερις αιτήσεις για δίπλωμα ευρεσιτεχνίας (δύο που αφορούσαν κυκλώματα, τα οποία στη συνέχεια έγιναν διπλώματα ευρεσιτεχνίας, και δύο που αφορούσαν βελτιστοποιήσεις μεταγλωττιστών και παρέμειναν απόρρητα της Intel Corp.)	
	Αύγουστος 1994-- Ιούνιος 1995	
	<u>University of Iowa</u>	Iowa City, Iowa
	Εξομοίωση κυκλωμάτων για αξιοπιστία και χαμηλή ισχύ. 1989—1994	
	<u>Coordinated Science Laboratory</u>	Urbana, Illinois
	Πιθανοθεωρητική εξομοίωση για αξιοπιστία και χαμηλή ισχύ. 1987—1989	
	<u>Εθνικό Μετσόβιο Πολυτεχνείο</u>	Αθήνα
	Συμμετοχή στο πρόγραμμα RACE, φάσεις 4.0 μέχρι 4.4. Αύγουστος 1994—Ιούνιος 1995	
Διδακτική	<u>University of Iowa</u>	Iowa City, Iowa
	Επισκέπτης Επίκουρος Καθηγητής	
	Εισαγωγή στον σχεδιασμό κυκλωμάτων VLSI (Φθινόπωρο 1994, μεταπτυχιακό).	
	Προχωρημένος σχεδιασμός κυκλωμάτων VLSI (Ανοιξη 1995, μεταπτυχιακό).	
	Ιούνιος 2001—Ιανουάριος 2003	
	<u>Πολυτεχνείο Κρήτης</u>	Χανιά
	Σχεδιασμός Συστημάτων VLSI και ASIC (Φθινόπωρο 2001 και 2002).	
	Σχεδιασμός Ψηφιακών Κυκλωμάτων με Εργαλεία CAD (Φθινόπωρο 2001 ΠΜΣ)	
	Λογική Σχεδίαση Ψηφιακών Συστημάτων (Ανοιξη 2002).	
	Σεπτέμβριος 2001 –	
	<u>Πανεπιστήμιο Θεσσαλίας</u>	Βόλος
	Ψηφιακή Σχεδίαση I (Φθινόπωρο 2001-2007 & 2013—2018)	
	Ψηφιακή Σχεδίαση II με CAD (Ανοιξη 2002 και 2003)	
	Ανάλυση Κυκλωμάτων (Φθινόπωρο 2009 και 2010)	
	Ηλεκτρονική I (Φθινόπωρο 2010 και 2012)	
	Σχεδίαση Συστημάτων VLSI (Ανοιξη 2003-2012, 2015-2019)	
	Έλεγχος και Επαλήθευση Ολοκληρωμένων Κυκλωμάτων (Φθινόπωρο 2003-2006)	
	Σχεδίαση Μικροεπεξεργαστών (Φθινόπωρο 2004 ΠΜΣ)	
	Σχεδίαση Συστημάτων VLSI (Ανοιξη 2006-2019 ΠΜΣ)	
	Σχεδίαση Εργαλείων CAD (Φθινόπωρο 2006-2018 ΠΜΣ)	
	Σχεδίαση Χαμηλής Ισχύος (Ανοιξη 2011-2015 ΠΜΣ)	
	Αρχιτεκτονική Υπολογιστών (Φθινόπωρο 2006 ΠΜΣ)	
	Εισαγωγή στο Ηλεκτρολογικό Σχέδιο και στα Ηλεκτροτεχνικά Υλικά (Ανοιξη 2013—2015)	
	Δίκτυα Αισθητήρων (Ανοιξη 2013—2015)	
	Εισαγωγή στη Φυσική Στερεάς Κατάστασης (Φθινόπωρο 2013—2018 ΠΜΣ)	
	Αλγόριθμοι CAD για Φυσική Σχεδίαση (Ανοιξη 2016-2018 ΠΜΣ)	
Διοικητική	Ιούλιος 2018 –	
	<u>Πανεπιστήμιο Θεσσαλίας</u>	Βόλος
	Κοσμήτορας Πολυτεχνικής Σχολής.	
	Ιούλιος 2013 – Ιούλιος 2018	
	<u>Πανεπιστήμιο Θεσσαλίας</u>	Λαμία
	Πρόεδρος Τμήματος Πληροφορικής.	
	Σεπτέμβριος 2007 – Αύγουστος 2011	
	<u>Πανεπιστήμιο Θεσσαλίας</u>	Βόλος
	Πρόεδρος Τμήματος Μηχανικών Η/Υ, Τηλεπικοινωνιών και Δικτύων.	
	Σεπτέμβριος 2003 – Αύγουστος 2007	
	<u>Πανεπιστήμιο Θεσσαλίας</u>	Βόλος
	Αναπληρωτής Πρόεδρος Τμήματος Μηχανικών Η/Υ, Τηλεπικοινωνιών και Δικτύων.	
	Σεπτέμβριος 2005 – Αύγουστος 2007	
	<u>Πανεπιστήμιο Θεσσαλίας</u>	Βόλος
	Διευθυντής Προγράμματος Μεταπτυχιακών Σπουδών	

	Σεπτέμβριος 2004 – Αύγουστος 2005 <u>Πανεπιστήμιο Θεσσαλίας</u>	Βόλος
	Εκπρόσωπος των Αναπληρωτών Καθηγητών στη Σύγκλητο Σεπτέμβριος 2007 – Αύγουστος 2011 <u>Πανεπιστήμιο Θεσσαλίας</u>	Βόλος
	Μέλος της Επιτροπής Ερευνών Συμμετοχή σε Επιτροπές Διαγωνισμών, Μετεγγραφών, Αξιολόγησης Υποψηφίων για το ΠΜΣ, Προγράμματος Σπουδών, Κατατακτηρίων Εξετάσεων, Αξιολόγησης Κατατασσομένων από το ΔΙΚΑΤΣΑ/ΔΟΑΤΑΠ, Επιλογής Διοικητικού Προσωπικού, Επιλογής Υποψηφίων για θέσεις του ΠΔ 407/80, ΣΕΜΣ. Συμμετοχή σε πάνω από δεκαπέντε (15) εισηγητικές επιτροπές εκλεκτορικών σωμάτων για μέλη ΔΕΠ και σε τρεις (3) εισηγητικές επιτροπές εκλεκτορικών σωμάτων για μέλη ΕΤΕΠ.	
Καινοτόμες επιχειρήσεις	Νοέμβριος 2007—Δεκέμβριος 2015 <u>Sensap A.E.</u>	Αθήνα
	Ιδρυτής, Chief Technical Officer Νοέμβριος 2008— Δεκέμβριος 2014 <u>Nanotropic A.E.</u>	Αθήνα
Επιστημονικός Σύμβουλος	Ιδρυτής, Chief Scientist Αύγουστος 1993 <u>Advanced Micro Devices</u>	San Jose, California
	Σύμβουλος για θέματα αξιοπιστίας και χαμηλής ισχύος. Ιούνιος 2003—Μάρτιος 2004 <u>Οργανισμός Προώθησης Ελληνικού Πολιτισμού</u>	Αθήνα
	Σύμβουλος για ψηφιακές πλατφόρμες μουσείων Σεπτέμβριος 2009—Μάρτιος 2010 <u>Παρατηρητήριο για την Κοινωνία της Πληροφορίας</u>	Αθήνα
	Σύμβουλος για υποδομές GIS Ιούνιος 2008—Δεκέμβριος 2014 <u>Helic A.E.</u>	Αθήνα
Reviews	Σύμβουλος σε θέματα προσομοίωσης ψηφιακών κυκλωμάτων. IEEE Transactions on Computers IEEE Transactions on VLSI Systems IEEE Transactions on Computer Aided Design Sensors & Actuators: A. Physical. Μέλος τεχνικής επιτροπής του Custom Integrated Circuit Conference (1996-1997) Μέλος τεχνικής επιτροπής και session chair στο International Symposium on Low Power Design (1999-2007) Μέλος τεχνικής επιτροπής του International On-Line Testing Workshop (2000-2006) Μέλος τεχνικής επιτροπής του Design and Test in Europe 2001, 2004-2006 Μέλος τεχνικής επιτροπής του ISQED 2001 Μέλος της οργανωτικής επιτροπής του PCI 2005	
Δημοσιώσεις Thesis	Georgios I. Stamoulis. “Design and Analysis of High Transmission Rate Telecommunication Circuit Elements.” Diploma Thesis. Department of Electrical and Computer Engineering. National Technical University of Athens, June 1989. Georgios I. Stamoulis. “New techniques for probabilistic simulation of VLSI CMOS circuits.” Master’s Thesis. Department of Electrical and Computer Engineering. University of Illinois at Urbana-Champaign, September 1991. Georgios I. Stamoulis. “Probabilistic simulation for reliability and average power estimation.” Ph.D. Thesis. Department of Electrical and Computer Engineering. University of Illinois at Urbana-Champaign, April 1994.	
Συνέδρια με κριτές	P. -C. Li, G. I. Stamoulis, and I. N. Hajj, “A probabilistic timing approach to hot-carrier effect estimation”. Proceedings of the IEEE/ACM Conference on Computer Aided Design, pp. 210-213, November 1992. G. I. Stamoulis, and I. N. Hajj, “Improved techniques for probabilistic simulation including signal correlation effects”. Proceedings of the 30th ACM/IEEE Design Automation Conference, pp. 379-383, June 1993.	

- P. -C. Li, G. I. Stamoulis, and I. N. Hajj, "iPROBE-d: A hot-carrier and oxide reliability simulator". Proceedings of the International Reliability Physics Symposium 1994, pp. 274-279.
- G. I. Stamoulis, and I. N. Hajj, "Slope considerations in probabilistic simulation". Proceedings of the 1994 Custom Integrated Circuits Conference, pp. 186-190, May 1994.
- G. I. Stamoulis, "A Monte-Carlo approach for the accurate and efficient estimation of average transition probabilities in sequential logic circuits". Proceedings of the 1996 Custom Integrated Circuits Conference, pp. 221-224.
- G. I. Stamoulis et al. "Transistor-level optimization". Proceedings of the Design Technology and Test Conference 1997. Invited paper.
- Y. Ye, K. Roy, and G. I. Stamoulis, "Quasi-static energy recovery logic and supply clock generation circuits". Proceedings of the 1997 International Symposium on Low Power Design, pp. 96-99.
- N. Bellas, I. Hajj, C. Polychronopoulos, and G. Stamoulis, "Architectural and compiler support for energy reduction in the memory hierarchy of high-performance microprocessors". Proceedings of the 1998 International Symposium on Low Power Design, pp. 70-75. June 1998.
- N. Bellas, I. Hajj, C. Polychronopoulos, and G. Stamoulis, "A new scheme for I-Cache energy reduction in High Performance Processors". Power-Driven Microarchitectures Workshop, International Symposium On Computer Architecture (ISCA), June 1998, Barcelona, Spain
- U. K. Narayanan, R. K. Roy, and G. I. Stamoulis, "Characterizing individual gate power sensitivity in low power design". Proceedings of VLSI-India 1999, pp. 625-628.
- N. Bellas, I. Hajj, C. Polychronopoulos, and G. Stamoulis, "Energy and Performance Improvements in Microprocessor Design using a Loop Cache". Proceedings of ICCD '99, pp. 378-383.
- P. D. Dimitropoulos, C. Kachris, and G. I. Stamoulis, "A New SOI capacitive sensor for absolute and differential pressure measurements," Digest 18th European Conf. Solid State Sensors (EUROSENSORS 2004), September 2004, Rome – Italy, pp. 60-61.
- P. D. Dimitropoulos, S. P. Nikolaidis, D. P. Karampatzakis, and G. I. Stamoulis, "A low-power CMOS VLSI circuit for signal conditioning in integrated capacitive sensors," IEEE Proc. 3rd International Conf. Sensors (Sensors 2004), October 2004, Vienna, Austria, pp. 202–205.
- P. D. Dimitropoulos, E. Hristoforou, and G. I. Stamoulis, "A hybrid Jiles-Atherton / Stoner-Wolfarth magnetic hysteresis model for inductive sensors and actuators," IEEE Proc. 3rd International Conf. Sensors (Sensors 2004), October 2004, Vienna, Austria, pp. 1566–1569.
- N. E. Evmorfopoulos, D. P. Karampatzakis, and G. I. Stamoulis, "Voltage-Drop-Constrained Optimization of Power Distribution Network Based on Reliable Maximum Current Estimates", ICCAD '04, pp. 479- 484.
- D. P. Karampatzakis, N. E. Evmorfopoulos and G. I. Stamoulis, "A Statistically-Based Engine for P/G Network Optimization", IEEE PRIME '05, pp. 51- 54.
- G. Dimitriou, P. K. Kikiras, G. I. Stamoulis, and I. N. Avaritsiotis. "A Tool for Calculating Energy Consumption in Wireless Sensor Networks", Proceedings of the 10th Panhellenic Conference on Informatics 2005, pp. 611-621.
- G. Stamoulis, M. Koziri, I. Katsavounidis, and N. Bellas, "A Low-Power VLSI Architecture for Intra Prediction in H.264", Proceedings of the 10th Panhellenic Conference on Informatics 2005, pp. 633-640.
- D. Karampatzakis, N. Evmorfopoulos, M. Tsiampas, and G. Stamoulis, "An RTL-to-grid design flow for power grid verification based on a statistical estimation engine", IEEE PRIME, Lecce, Italy, 2006. IEEE PRIME '06, pp. 37-40.
- M. G. Koziri, G. I. Stamoulis, and I. X. Katsavounidis, "A Low-Power VLSI Architecture for Intra Prediction in H.264", IEEE PRIME '06, pp. 109-112.
- D. Bountas, and G. I. Stamoulis, "CARROT: A Tool for Fast and Accurate Soft Error Rate Estimation", 6th International Workshop, SAMOS 06, pp.331-338.

- N. Evmorfopoulos, D. P. Karampatzakis, and G. I. Stamoulis, "Precise Identification of the Worst-Case Voltage Drop Conditions in Power Grid Verification", ICCAD '06, pp. 112-118.
- M. G. Koziri, G. I. Stamoulis, and I. X. Katsavounidis, "Power reduction in H.264 encoder through algorithmic and circuits transformation" ISLPED 2006, pp. 107-112.
- M. G. Koziri, A. N. Dadaliaris, G. I. Stamoulis, and I. X. Katsavounidis, "A Novel Low-Power Motion Estimation Design for H.264", ASAP 2007.
- P. Kikiras, D. Drakoulis, D. Dres, and G. I. Stamoulis, "Wireless Sensor Networks: Business Models and Market Issues". 6th Conf. Telecomm Technoeconomics, 2007, Helsinki, Finland.
- K. Katsalis, A. Xenakis, K. Kikiras, and G. Stamoulis, "Topology Optimization in Wireless Sensor Networks for Precision Agriculture Applications" "International Conference on Sensor Technologies and Applications SENSORCOMM 2007, Valencia, Spain, pp. 526-530.
- K. Katsalis, A. Xenakis, P. Kikiras, and G. Stamoulis, "Μεθοδολογίες βελτιστοποίησης της τοπολογίας ασυρμάτων δικτύων αισθητήρων σε εφαρμογές γεωργίας ακριβείας" (Wireless sensor network topology optimization for precision agriculture applications). 5ο Συνέδριο Ε.Γ.Μ.Ε. (5th National Agricultural Engineering Congress), 2007, Larissa, Greece.
- K. Katsalis, A. Xenakis, K. Kikiras, and G. Stamoulis, "Topology Optimization in Wireless Sensor Networks for Precision Agriculture Applications". 6th European Conference on Precision Agriculture, 2007, Skiathos, Greece.
- D. P. Karampatzakis, M. K. Tsiampas, N. E. Evmorfopoulos, and G. I. Stamoulis, "A Design Flow for the Precise Identification of the Worst-Case Voltage Drop in Power Grid Analyses". Panhellenic Conference on Informatics, 2008 (PCI '08), pp. 135 – 139.
- P. Kikiras, L. Perlepes, I. Kalavros, A. Tzimas, and G. Stamoulis, "Sensor Networks Applications in Livestock Production: the Case of Fleming Institute's Animal House". 2nd Panhellenic Conference of Livestock Breeding, 2008, Larissa, Greece.
- P. Kikiras, L. Perlepes, and G. Stamoulis, "Wireless Sensor Networks in Gaming". 1st Workshop on Wireless Sensor Networks, 2008, Athens, Greece.
- D. Bountas, G. Stamoulis, and N. Evmorfopoulos, "A macromodel technique for VLSI dynamic simulation by mapping pre-characterized transitions". IEEE International Conference on Computer Design, 2008 (ICCD 2008), pp. 450 – 456.
- N. Papadakis, K. Ntalianis, A. Doulamis, and G. Stamoulis, "An Automatic Multi-Agent Web Image and Associated Keywords Retrieval System". 16th International Conference on Systems, Signals and Image Processing, 2009 (IWSSIP 2009), pp. 1-4.
- M. Owaida, M. Koziri, I. Katsavounidis, and G. Stamoulis, "A high performance and low power hardware architecture for the transform and quantization stages in H.264". IEEE International Conference on Multimedia and Expo, 2009 (ICME 2009), pp. 1102 – 1105.
- A. Xenakis, L. Perlepes, P. Kikiras, and G. Stamoulis. "Application of the LGR Algorithm for topology control in a wireless sensor network for precision agriculture applications". Panhellenic Conference of the Society of Agricultural Engineers, 2009, Thessaloniki Greece, pp. 641-648.
- C. Chatziplaton, G. I. Stamoulis, and P. Kikiras, "Vehicle speed and position determination using WSN". 2nd Workshop on Wireless Sensor Networks, 2009, Athens, Greece.
- G. Giannakas, F. Plessas, G. Nassopoulos, and G. Stamoulis, "A 2.45GHz power harvesting circuit in 90nm CMOS". 17th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2010, pp. 835 – 838.
- M. K. Tsiampas, D. Bountas, P. Merakos, N. E. Evmorfopoulos, S. Bantas, and G. I. Stamoulis, "A power grid analysis and verification tool based on a Statistical Prediction Engine". 17th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2010, pp. 839 – 842.
- A. Zaharis, A. I. Martini, P. Kikiras, and G. Stamoulis, "User Authentication Method and Implementation Using a Three-Axis Accelerometer". MOBILIGHT 2010, pp. 192-202.

- A. Raman, D. Bountas, and G. Stamoulis, "A High-Capacity Power Integrity Flow Supporting Inductive Rail Effects With Transistor-Level Accuracy". Magma Users Summit on Integrated Circuits 2010 (Best paper award).
- A. Zaharis, A. I. Martini, L. Perlepes, G. Stamoulis, and P. Kikiras, "Live forensics framework for wireless sensor nodes using sandboxing". Proceedings of the 6th ACM workshop on QoS and security for wireless and mobile networks 2010 (Q2SWinet '10), pp. 70-77.
- N. Evmorfopoulos, M. -A. Rammou, G. Stamoulis, and J. Moondanos, "Characterization of the worst-case current waveform excitations in general RLC-model power grid analysis". 2010 IEEE/ACM International Conference on Computer-Aided Design (ICCAD), pp. 824 – 830.
- V. N. Petoussis, P. D. Dimitropoulos, G. Stamoulis, and E. Houstis, "Novel Dynamic Technique Reducing the Offset Voltage in a Hall Effect Sensor". 18th Sensor + Test, 2011, pp. 780 – 785.
- V. N. Petoussis, P. D. Dimitropoulos, G. Stamoulis, and E. Houstis, "Introducing a New Hall Effect Sensor- Novel Dynamic Offset Reduction Method", 1st International Conference on Pervasive and Embedded Computing and Communication Systems (PECCS) Algarve Portugal, 2011.
- A. Zaharis, L. Perlepes, G. Stamoulis, and P. Kikiras, "End – to – End Secure Data Delivery in Wireless Sensor Networks". SENSORCOMM 2011, pp. 356-364.
- F. Liakou, P. Dimitropoulos, and G. Stamoulis, "2D Simulator for Eddy Current Field Filters". Eurosensors XXV, 2011.
- M. -A. Nef, S. Karagiorgou, G. I. Stamoulis, and P. K. Kikiras, "Supporting Service Differentiation in Wireless Sensor Networks". 15th Panhellenic Conference on Informatics (PCI), 2011, pp. 127 – 133.
- M. -A. Nef, I. Filippopoulos, E. Voumvourakis, A. Aggelis, L. Perlepes, G. Stamoulis, and P. Kikiras, "i-Protect: An Open Source Emergency Management Framework". 15th Panhellenic Conference on Informatics (PCI), 2011, pp. 386 – 391.
- F. Toufexis, A. Papanikolaou, D. Soudris, G. Stamoulis, and S. Bantas, "Power, Performance and Area Prediction of 3D ICs during Early Stage Design Exploration in 45nm". 18th IEEE International Conference on Electronics, Circuits, and Systems (ICECS), 2011, pp. 715 - 718.
- N. Larisis, L. Perlepes, P. Kikiras, and G. Stamoulis, "U-Park: Parking Management System Based on Wireless Sensor Network Technology". SensorComm 2012, pp. 170-177.
- M. -A. Nef, L. Perlepes, S. Karagiorgou, G. I. Stamoulis, and P. K. Kikiras, "Enabling QoS in the Internet of Things". Fifth International Conference on Communication Theory, Reliability, and Quality of Service (CTRQ) 2012, pp. 33-38.
- K. Daloukas, M.-A. Rammou, G. Drasidis, M. Tsiampas, N. Evmorfopoulos, P. Tsompanopoulou, and G. Stamoulis, "Parallel Preconditioners Based on Fast Poisson Solvers for Efficient Large-Scale Power Grid Analysis". ACM/IEEE 2012 Design Automation Conference, Poster. San Francisco, June 2012.
- K. Daloukas, N. Evmorfopoulos, P. Tsompanopoulou, and G. Stamoulis, "Fast Transform-Based Solvers as Parallel Preconditioners for Large-Scale Power Grid Analysis on Massively Parallel Architectures". Poster at the ACM Student Research Competition, held in conjunction with the ACM/IEEE 2012 Design Automation Conference. San Francisco, June 2012.
- K. Daloukas, N. Evmorfopoulos, G. Drasidis, M. Tsiampas, P. Tsompanopoulou, and G. Stamoulis, "Fast Transform-Based Preconditioners for Large-Scale Power Grid Analysis on Massively Parallel Architectures", ICCAD 2012, pp. 384-391. (Best Paper Award Nominee)
- N. Zarokostas, P. Dimitropoulos, J. Soldatos, and G. Stamoulis, "Triggering and Data Collection Framework for Sensing Application Level Events", SOFTCOM 2012, pp. 1-5.
- A. Xenakis, I. Katsavounidis, and G. Stamoulis, "Investigating Wireless Sensor Network Lifetime under Static Routing with Unequal Energy Distribution", Proc. Asia-Pacific Signal Information Processing Association Annual Summit and Conference 2012, pp. 1–7.

- A. Cevrero, N. Evmorfopoulos, C. Antoniadis, P. Ienne, Y. Leblebici, A. Burg, and G. Stamoulis, "Fast and Accurate BER Estimation Methodology for I/O Links Based on Extreme Value Theory". DATE 2013, pp. 503-508.
- K. Daloukas, A. Marnari, N. Evmorfopoulos, P. Tsompanopoulou, and G. Stamoulis, "A Parallel Fast Transform-Based Preconditioning Approach for Electrical-Thermal Co-Simulation of Power Delivery Networks". DATE 2013, pp. 1689-1694.
- A. Xenakis, F. Foukalas, G. Stamoulis, and T. Khattab, "Energy-aware Joint Power, Packet and Topology Optimization by Simulated Annealing for WSNs". 7th IEEE GCC Conference and Exhibition (GCC) 2013, pp. 17-21.
- K. Daloukas, N. Evmorfopoulos, P. Tsompanopoulou, and G. I. Stamoulis, "A 3-D Fast Transform-Based Preconditioner for Large-Scale Power Grid Analysis on Massively Parallel Architectures". ISQED 2014, pp. 723-730.
- I. Apostolopoulou, K. Daloukas, N. Evmorfopoulos, and G. I. Stamoulis, "Selective Inversion of Inductance Matrix for Large-Scale Sparse RLC Simulation". DAC 2014, pp. 1-6.
- C. Kalonakis, C. Antoniadis, P. Giannakou, D. Dioudis, G. Pinitas, and G. Stamoulis, "TKtimer: Fast & Accurate Clock Network Pessimism Removal". ICCAD 2014, pp. 606-610.
- G. I. Paliaroutis, P. Tsoumanis, G. Dimitriou, and G. I. Stamoulis, "SER analysis for multiple affected gates". Proceedings of CSCESM 2014.
- A. N. Dadaliaris, G. Dimitriou, and G. I. Stamoulis, "VDA-Place: Voltage-Drop-Aware Standard Cell Placement". Proceedings of CSCESM 2014.
- S. K. Ioannidis, D. Ntioudis, C. Antoniadis, A. N. Dadaliaris, P. Tsompanopoulou, N. E. Evmorfopoulos, and G. I. Stamoulis, "Optimization of an Integrated Circuit Placement Algorithm in a Parallel Environment". Proceedings of CSCESM 2014.
- M. Zervas, M. Spanou, G. Dimitriou, and G. I. Stamoulis, "Compact Physical Model of TSV for quick and accurate exploration of 3DICs". Proceedings of CSCESM 2014.
- G. Floros, G. Dimitriou, and G. I. Stamoulis, "Electromigration: Estimation methodology for the sub-45nm era". Proceedings of CSCESM 2014.
- C. Antoniadis, G. Karakonstantis, N. Evmorfopoulos, A. Burg and G. Stamoulis, "On the Statistical Memory Architecture Exploration and Optimization". DATE 2015, pp. 543-548.
- G.-I. Paliaroutis, P. Tsoumanis and G. I. Stamoulis, "SER Analysis for Multiple Affected Gates". Proceedings of PACET 2015.
- A. Dadaliaris, P. Oikonomou, G. Dimitriou and G. Stamoulis, "VDA Place+: Voltage-Drop-Aware Placement". Proceedings of PACET 2015.
- T. Strousidou, C. Antoniadis, I. Arvanitakis, G. Dimitriou, N. Evmorfopoulos, P. Tsompanopoulou, P. Bozani and G. Stamoulis, "Accelerating GORDIAN-based placement through null space removal techniques". Proceedings of PACET 2015.
- D. Ntioudis, C. Kalonakis, P. Giannakou, C. Antoniadis, G. Stamoulis, P. Tsompanopoulou, N. Evmorfopoulos, J. Moondanos, and G. Dimitriou, "CCSOpt: A Continuous Gate-Level Resizing Tool", Proceedings of MOCAS 2015.
- P. Giannakou, C. Antoniadis, C. Kalonakis, D. Dioudis, G. Stamoulis, P. Tsompanopoulou, N. Evmorfopoulos, J. Moondanos, and G. Dimitriou, "GDS2trim: Physical Layout Manipulation Utility for continuous transistor sizing", Proceedings of MOCAS 2015.
- N. C. Zygouris, F. Vlachos, A. N. Dadaliaris, G. I. Stamoulis, D. Vavougiou, E. Nerantzaki, and A. Striftou, "Learning difficulties screening web application", Accepted for publication at WEEF/ICL2015.
- P. Oikonomou, T. Loukopoulos, A. N. Dadaliaris, M. G. Koziri, and G. I. Stamoulis, "On formulating and tackling integrated circuit placement as a scheduling problem", Proceedings of the 19th Panhellenic Conference on Informatics, 2015, pp. 86-91.
- K. Kalaitzidis, G. Dimitriou, G. Stamoulis, and M. Dossis, "Performance and power simulation of a functional-unit-network processor with SimpleScalar and Wattch", Proceedings of the 19th Panhellenic Conference on Informatics, 2015, pp. 71-76.
- A. Xenakis, F. Foukalas, and G. Stamoulis, "Minimum weighted clustering algorithm for wireless sensor networks", Proceedings of the 19th Panhellenic Conference on Informatics, 2015, pp. 255-260.

- G. I. Paliaroutis, P. Tsoumanis, G. Dimitriou, and G. I. Stamoulis, "SER Analysis of Multiple Transient Faults in Combinational Logic", Proceedings of the SouthEast European Design Automation, Computer Engineering, Computer Networks and Social Media Conference (SEEDA-CECNSM '16), pp. 36-41
- P. Oikonomou, M. G. Koziri, A. N. Dadaliaris, Y. Hatzaras, E. Nerantzaki, and G. I. Stamoulis, "Heuristics for Iterative Detailed Standard Cell Placement", Proceedings of the SouthEast European Design Automation, Computer Engineering, Computer Networks and Social Media Conference (SEEDA-CECNSM 16), pp. 19-24
- A. N. Dadaliaris, E. Nerantzaki, P. Oikonomou, Y. Hatzaras, A.-O. Troumpoulou, I. Arvanitakis, and G. I. Stamoulis, "Enhanced Tetris Legalization", Proceedings of the SouthEast European Design Automation, Computer Engineering, Computer Networks and Social Media Conference (SEEDA-CECNSM '16), pp. 32-35
- G. Dimitriou, G. Chantzianastasiou, A. Tsakyridis, G. Stamoulis, and M. Dossis, "Source-Level Compiler Optimizations for High-Level Synthesis", Proceedings of the SouthEast European Design Automation, Computer Engineering, Computer Networks and Social Media Conference (SEEDA-CECNSM '16), pp. 11-18.
- M. Koziri, P. Papadopoulos, N. Tziritas, A. N. Dadaliaris, T. Loukopoulos, and G. I. Stamoulis, "A framework for scheduling the encoding of multiple smart user videos", 11th International Workshop on Semantic and Social Media Adaptation and Personalization (SMAP), 2016.
- A. Dadaliaris, E. Nerantzaki, M. Koziri, P. Oikonomou, T. Loukopoulos and G. Stamoulis, "Performance Evaluation of Tetris-based Legalization Heuristics", Proceedings of the 20th Pan-Hellenic Conference on Informatics 2016, pp. 60:1--60:6.
- P. Papadopoulos, M. Koziri, N. Tziritas, T. Loukopoulos, I. Anagnostopoulos and G. Stamoulis, "Performance Evaluation of Batch Encodings in HEVC Using Slice Level Parallelism", Proceedings of the 20th Pan-Hellenic Conference on Informatics 2016 pp. 70:1-70:6.
- N. C. Zygouris, F. Vlachos, A. N. Dadaliaris, P. Oikonomou, G. I. Stamoulis, D. Vavougiou, E. Nerantzaki, and A. Striftou, "The Implementation of a Web Application for Screening Children with Dyslexia", ICL 2016: Interactive Collaborative Learning pp 415-423.
- G. Chatzianastasiou, A. Tsakyridis, G. Dimitriou, G. Stamoulis, and M. Dossis, "Compiler transformations in hardware synthesis of Mpeg2 codes", 5th International Conference on Modern Circuits and Systems Technologies (MOCASST) pp. 1-4, 2016.
- N. C. Zygouris, A. Striftou, A. N. Dadaliaris, G. I. Stamoulis, A. C. Xenakis, and D. Vavougiou, "The use of LEGO mindstorms in elementary schools", Global Engineering Education Conference (EDUCON), 2017 IEEE, 514-516, 2017.
- N. C. Zygouris, G. I. Stamoulis, F. Vlachos, D. Vavougiou, A. N. Dadaliaris, E. Nerantzaki, P. Oikonomou, and A. Striftou, "Screening for disorders of mathematics via a web application", Global Engineering Education Conference (EDUCON), 2017 IEEE, 502-507, 2017.
- G. Dimitriou, M. Dossis, and G. Stamoulis, "Loop pipelining in high-level synthesis with CCC", 6th International Conference on Modern Circuits and Systems Technologies, 2017.
- P. Oikonomou, M. G. Koziri, A. N. Dadaliaris, T. Loukopoulos, and G. I. Stamoulis, "Domocus: Lock free parallel legalization in standard cell placement", 6th International Conference on Modern Circuits and Systems Technologies, 2017.
- A. N. Dadaliaris, P. Oikonomou, M. G. Koziri, E. Nerantzaki, T. Loukopoulos, and G. I. Stamoulis, "A Connectivity-Based Legalization Scheme for Standard Cell Placement", Circuits and Systems 8 (08), 191, 2017.
- I. Filippopoulos, and G. Stamoulis, "Collecting and using vessel's live data from on board equipment using Internet of Vessels (IoV) platform" South Eastern European Design Automation, Computer Engineering, Computer Networks and Social Media Conference (SEEDA-CECNSM) 2017, pp. 1-6.

- G. Dimitriou, M. Dossis, and G. Stamoulis “Minimal-area loop pipelining for high-level synthesis with CCC” South Eastern European Design Automation, Computer Engineering, Computer Networks and Social Media Conference (SEEDA-CECNSM) 2017, pp. 1-8.
- A. N. Dadaliaris, P. Oikonomou, M. G. Koziri, Y. Hatzaras, and G. I. Stamoulis “SCIZER: A scalable placement visualizer/analyzer” South Eastern European Design Automation, Computer Engineering, Computer Networks and Social Media Conference (SEEDA-CECNSM) 2017, pp. 1-4.
- M. Koziri, P. K. Papadopoulos, N. Tziritas, N. Giachoudis, T. Loukopoulos, S. U. Khan, and G. I. Stamoulis “Heuristics for tile parallelism in HEVC”, 2017 25th European Signal Processing Conference (EUSIPCO), pp. 1514-1518
- K. Mantos, N. Koungalis, G. Demesiotis, P. Oikonomou, A. N. Dadaliaris, and G. I. Stamoulis “WEVIAN: A Web-based Placement Visualizer/Analyzer” Proceedings of the 21st Pan-Hellenic Conference on Informatics, Article No. 16
- E. Fatourou, N. C. Zygoris, T. Loukopoulos, and G. I. Stamoulis “Evaluation of Early Introduction to Concurrent Computing Concepts in Primary School” International Conference on Interactive Collaborative Learning, pp. 543-552.
- G. I. Paliaroutis, P. Tsoumanis, N. Evmorfopoulos, G. Dimitriou, and G. I. Stamoulis “Placement-based SER estimation in the presence of multiple faults in combinational logic” 2017 27th International Symposium on Power and Timing Modeling, Optimization and Simulation (PATMOS) pp. 1-6.
- C. Antoniadis, D. Garyfallou, N. Evmorfopoulos, and G. Stamoulis “EVT-based worst case delay estimation under process variation” Design, Automation & Test in Europe Conference & Exhibition (DATE), 2018, pp. 1333-1338.
- M. Tsiampas, N. Evmorfopoulos, K. Daloukas, J. Moondanos, and G. Stamoulis “A power-supply noise aware dynamic timing analysis methodology, based on a statistical prediction engine” 13th International Conference on Design & Technology of Integrated Systems In Nanoscale Era (DTIS) 2018, pp. 1-6.
- G. Floros, K. Daloukas, N. Evmorfopoulos, and G. Stamoulis “A parallel iterative approach for efficient full chip thermal analysis” (best paper award) 7th International Conference on Modern Circuits and Systems Technologies (MOCASST) 2018, pp. 1-4.
- D. Garyfallou, N. Evmorfopoulos, and G. Stamoulis “Large scale circuit simulation exploiting combinatorial multigrid on massively parallel architectures” 7th International Conference on Modern Circuits and Systems Technologies (MOCASST) 2018, pp. 1-4.
- P. Oikonomou, A. N. Dadaliaris, T. Loukopoulos, A. Kakarountas, and G. I. Stamoulis “A Tetris-based legalization heuristic for standard cell placement with obstacles” 7th International Conference on Modern Circuits and Systems Technologies (MOCASST) 2018, pp. 1-4.
- T. Loukopoulos, N. Tziritas, M. Koziri, G. Stamoulis, S. U. Khan, C.-Z. Xu, and A. Y. Zomaya “Data Stream Processing at Network Edges” 2018 IEEE International Parallel and Distributed Processing Symposium Workshops, pp. 657-665.
- G. Floros, N. Evmorfopoulos, and G. Stamoulis “Efficient Hotspot Thermal Simulation Via Low-Rank Model Order Reduction” 15th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design, pp. 205-208.
- D. Garyfallou, N. Evmorfopoulos, and G. Stamoulis “A Combinatorial Multigrid Preconditioned Iterative Method for Large Scale Circuit Simulation on GPUs” 15th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design, pp. 209-212.
- C. Antoniadis, N. Evmorfopoulos, and G. Stamoulis “On the Sparsification of the Reluctance Matrix in RLCK Circuit Transient Analysis” 15th International Conference on Synthesis, Modeling, Analysis and Simulation Methods and Applications to Circuit Design, pp. 201-204.
- G. Dimitriou, M. Dossis, and G. Stamoulis “Operation Dependencies in Loop Pipelining for High-Level Synthesis” 3rd South-East Europe Design Automation, Computer Engineering, Computer Networks and Social Media Conference, pp. 15-20.

- E. N. Lallas, A. Xenakis, and G. Stamoulis "QoS and MPLS design issues in NoCs" 3rd South-East Europe Design Automation, Computer Engineering, Computer Networks and Social Media Conference, pp. 21-24.
- I. Filippopoulos and G. I. Stamoulis "Transferring Structured Data and applying business processes in remote vessel's environments using the InfoNet Platform" 3rd South-East Europe Design Automation, Computer Engineering, Computer Networks and Social Media Conference, pp. 134-139.
- G. I. Paliaroutis, P. Tsoumanis, N. Evmorfopoulos, G. Dimitriou and G. Stamoulis "A Placement-aware Soft Error Rate Estimation of Combinational Circuits for Multiple Transient Faults in CMOS Technology" 31st IEEE International Symposium on Defect and Fault Tolerance in VLSI and Nanotechnology Systems.
- T. Loukopoulos, N. Tziritas, M. Koziri, G. Stamoulis, and S. Khan "A Pareto-Efficient Algorithm for Data Stream Processing at Network Edges" 2018 IEEE International Conference on Cloud Computing Technology and Science (CloudCom), pp. 159-162.
- C. Antoniadis, N. Evmorfopoulos and G. Stamoulis "Efficient sparsification of dense circuit matrices in model order reduction" Proceedings of the 24th Asia and South Pacific Design Automation Conference, pp. 255-260.
- J. N. Avaritsiotis, G. Stamoulis, and S. A. Achtidas. "Automatic Resistor generation and thick film circuit layout using the Magic layout editor". Microelectronics Journal, pp. 5-14, June 1991.
- P. -C. Li, G. I. Stamoulis, and I. N. Hajj. "A probabilistic timing approach to hot-carrier effect estimation". IEEE Transactions on Computer Aided Design, pp. 1223-1234, October 1994.
- N. Bellas, I. Hajj, C. Polychronopoulos, and G. Stamoulis. "Architectural and Compiler Techniques for Energy Reduction in High Performance Microprocessors". IEEE Transactions on VLSI Systems, June 2000, pp. 317-326.
- G. I. Stamoulis. "A Monte-Carlo Approach for the Estimation of Average Transition Probabilities in Sequential Logic Circuits". Active and Passive Electronic Components, vol. 24, no. 2, pp. 69-85, 2001.
- N. E. Evmorfopoulos, G. I. Stamoulis, and J. N. Avaritsiotis. "A Monte-Carlo approach for maximum power estimation based on extreme value theory". IEEE Transactions on Computer Aided Design, vol. 21, no. 4, pp. 415-432, 2002.
- P. D. Dimitropoulos, C. Kachris, D. P. Karampatzakis, and G. I. Stamoulis, "A new SOI capacitive sensor for absolute and differential pressure measurements". Sensors and Actuators A, Vol. 123-124 (2005), pp. 36-43.
- P. D. Dimitropoulos, D. P. Karampatzakis, G. D. Panagopoulos, and G. I. Stamoulis, "A Low-Power / Low-Noise Readout Circuit for Integrated Capacitive Sensors". IEEE J. Sensors, Vol. 6 (2006), pp. 755-769.
- P. D. Dimitropoulos, G. I. Stamoulis, and E. Hristoforou, "A 3D Hybrid Jiles-Atherton / Stoner-Wohlfarth Magnetic Hysteresis Model for Inductive Sensors and Actuators". IEEE J. Sensors, Vol. 6 (2006), pp. 721-736.
- N. E. Evmorfopoulos, D. P. Karampatzakis, and G. I. Stamoulis, "Accurate Minimum Area Design of Power/Ground Meshes Subject to Voltage-Drop Constraints". Journal of Active and Passive Electronic Devices, Vol. 2 (2006), pp. 55-70.
- V. N. Petoussis, P. Dimitropoulos, and G. Stamoulis, "A Novel Hall Effect Sensor Using Elaborate Offset Cancellation Method". Sensors and Transducers, Vol. 100, Issue 1 (2009), pp. 85-91.
- V. N. Petoussis, P. Dimitropoulos, and G. Stamoulis, "General Development of a New Hall Effect Sensor". Sensors and Transducers, Vol. 127, Issue 4 (2011), pp. 36-44.
- G. Giannakas, F. Plessas, and G. Stamoulis, "Pseudo-FG technique for efficient energy harvesting". Electronics letters 48 (9), pp. 522-523.
- L. Perlepes, A. Zaharis, G. Stamoulis, and P. Kikiras, "A Framework for Secure Data Delivery in Wireless Sensor Networks". Sensors and Transducers, Vol. 136, Issue 2 (2012), pp. 125-149.
- N. Larisis, L. Perlepes, P. Kikiras, and G. Stamoulis, "U-Park: Parking Management System Based on Wireless Sensor Network Technology". Sensors and Transducers Journal, Special Issue, vol. 18 (2013), pp. 100-112.

- N. C. Zygouris, F. Vlachos, A. N. Dadaliaris, P. Oikonomou, G. I. Stamoulis, D. Vavougiou, E. Nerantzaki, and A. Striftou, "The Implementation of a Web Application for Screening Children with Dyslexia", *Interactive Collaborative Learning*, Volume 545 of the series *Advances in Intelligent Systems and Computing* (2016) pp 415-423
- A. Xenakis, F. Foukalas, and G. Stamoulis, "Cross-layer energy-aware topology control through Simulated Annealing for WSNs", *Computers & Electrical Engineering* Volume 56, November 2016, pp. 576–590.
- K. Daloukas, N. Evmorfopoulos, P. Tsompanopoulou, and G. Stamoulis, "Parallel Fast Transform-Based Preconditioners for Large-Scale Power Grid Analysis on Graphics Processing Units (GPUs)", *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* 35(10) (2016) pp. 1653-1666.
- N. C. Zygouris, E. Avramidis, A. V. Karapetsas, and G. I. Stamoulis, "Differences in dyslexic students before and after a remediation program: A clinical neuropsychological and event related potential study", *Applied Neuropsychology: Child*, pp. 1-10 (2017)
- A. Xenakis, F. Foukalas, G. Stamoulis, and I. Katsavounidis, "Topology control with coverage and lifetime optimization of wireless sensor networks with unequal energy distribution", *Computers & Electrical Engineering* 2017.
- A. N. Dadaliaris, P. Oikonomou, M. G. Koziri, E. Nerantzaki, Y. Hatzaras, D. Garyfallou, T. Loukopoulos, and G. I. Stamoulis, "Heuristics to Augment the Performance of Tetris Legalization: Making a Fast but Inferior Method Competitive", *Journal of Low Power Electronics* 13 (2), 220-230, 2017
- M. Koziri, P. K. Papadopoulos, N. Tziritas, A. N. Dadaliaris, T. Loukopoulos, and G. I. Stamoulis, "On planning the adoption of new video standards in social media networks: a general framework and its application to HEVC", *Social Network Analysis and Mining* 7 (1), 32, 2017
- N. Tziritas, M. Koziri, A. Bachtsevani, T. Loukopoulos, S. U. Khan, G. Stamoulis, and C.-Z. Xu, "Performing Data Replication and Virtual Machine Migrations to Mitigate Network Overhead between Traditional Cloud and Mobile Cloud Systems" *IEEE Transactions on Sustainable Computing* 2(4): 320-332, 2017.
- N. C. Zygouris, F. Vlachos, A. N. Dadaliaris, P. Oikonomou, G. I. Stamoulis, D. Vavougiou, E. Nerantzaki, and A. Striftou "A Neuropsychological Approach of Developmental Dyscalculia and a Screening Test Via a Web Application" *International Journal of Engineering Pedagogy (iJEP)* 7 (4), pp. 51-65.
- P. K. Papadopoulos, N. C. Zygouris, M. G. Koziri, T. Loukopoulos, and G. I. Stamoulis "Mobivoke: A Mobile System Architecture to Support off School Collaborative Learning Process" *Interactive Mobile Communication, Technologies and Learning*, pp. 587-592
- N. C. Zygouris, E. Avramidis, A. V. Karapetsas, and G. I. Stamoulis "Differences in dyslexic students before and after a remediation program: a clinical neuropsychological and event related potential study", *Applied Neuropsychology: Child* 7 (3), pp. 235-244
- E. Fatourou, N. C. Zygouris, T. Loukopoulos, and G. I. Stamoulis "Teaching Concurrent Programming Concepts Using Scratch in Primary School: Methodology and Evaluation" *International Journal of Engineering Pedagogy (iJEP)* 8 (4), 89-105
- G. Floros, K. Daloukas, N. Evmorfopoulos, and G. Stamoulis "A Preconditioned Iterative Approach for Efficient Full Chip Thermal Analysis on Massively Parallel Platforms" *Technologies* 7 (1) 1
- P. Oikonomou, A. Dadaliaris, K. Kolomvatsos, T. Loukopoulos, A. Kakarountas, and G. I. Stamoulis "Improved Parallel Legalization Schemes for Standard Cell Placement with Obstacles" *Technologies* 7 (1) 3
- N. Evmorfopoulos, J. Avaritsiotis, and G. Stamoulis, "Maximum power estimation in CMOS VLSI circuits". In A. Nassiopoulou and X. Zanni (eds.), *Microelectronics, Microsystems and Nanotechnology*, World Scientific, 2001.
- N. Evmorfopoulos, S. Bantas, and G. Stamoulis, "Simulation Techniques for Large-Scale Circuits", *Mixed-Signal Circuits* 46, 205, CRC Press 2016.
- Translation to Greek of the 3rd edition of "Computer Architecture" by J. Hennessy and D. Patterson.

Κεφάλαια
σε βιβλία

Βιβλία

U.S. Patents	<p>G. I. Stamoulis, J. Sugisawa, and M. Y. Zhang. "Method and Apparatus for Low Power Data Transmission". United States Patent 5831453.</p> <p>G. I. Stamoulis, and Y. Ye. "Method and Apparatus for Generating Waveforms using Adiabatic Circuitry". United States Patent 5838203.</p> <p>G. Stamoulis, S. Bantas, D. Bountas, N. Evmorfopoulos, M. Tsiampas, and P. Merakos. "System and method for determining simulated response extrema for integrated circuit power supply networks". United States Patent US8516423.</p> <p>G. Stamoulis, S. Bantas, D. Bountas, N. Evmorfopoulos, M. Tsiampas, and P. Merakos. "System and Method for Circuit Analysis". United States Patent Application 12/894102.</p> <p>K. Daloukas, N. Evmorfopoulos, P. Tsompanopoulou, and G. Stamoulis, "Large-scale power grid analysis on parallel architectures". United States Patent US9858369.</p>
Επίβλεψη διδασκτορικών Τριμελείς και επταμελείς επιτροπές Αναφορές	<p>Δεκατρείς (13) ολοκληρώθηκαν.</p> <p>Πανεπιστήμιο Θεσσαλίας, Πολυτεχνείο Κρήτης, Πανεπιστήμιο Πατρών, Εθνικό Μετσόβιο Πολυτεχνείο, Purdue University, École Polytechnique Fédérale de Lausanne.</p> <p>Πάνω από 1000</p>
Ερευνητικά προγράμματα	<ul style="list-style-type: none"> • Intel Corp. δωρεά \$45000 και μηχανημάτων για το εργαστήριο • Θέων Αισθητήρες AEBE. €229000. Ανάπτυξη αισθητήρων πίεσης με τεχνολογία MEMS • ΠΕΝΕΔ 2003. €106000. Βελτιστοποίηση κυκλωμάτων VLSI ως προς το κύκλωμα τροφοδοσίας. • NETNET €15000. Σεμινάρια επιμόρφωσης Ηλεκτρολόγων Μηχανικών • ΥΠΠΟ €6000. Ανάπτυξη συστήματος περιβαλλοντικών μετρήσεων. • iMuse €500000. Ανάπτυξη συστήματος πλοήγησης σε μουσεία βασισμένο σε τεχνολογία RFID. • Nanotropic Spin-off €600000. Αξιόπιστος υπολογισμός πτώσης τάσης σε κυκλώματα τροφοδοσίας ολοκληρωμένων κυκλωμάτων. • ECOSYSTEM €40000. Ανάπτυξη συστήματος Smart Home βασισμένο σε FPGAs. • NanoTrim €1200000. Βελτιστοποίηση μεγέθους τρανζίστορ ολοκληρωμένων κυκλωμάτων στο φυσικό επίπεδο. • Horizon 2020: Solder €450,000 • Horizon 2020: Enforce €50,000 • ΓΓΕΤ: Ερευνώ Καινοτομώ €270,000